We claim

1. A data surge protection module for protecting data communication circuits from transient voltage surges, comprising:

a) a housing;

b) a printed circuit board within said housing;

c) a ground terminal plate coupled to said printed circuit board adapted for insertion in a grounding strap on an enclosure; and

d) a low impedance ground wire coupled to said printed circuit board and adapted to be coupled to said enclosure whereby one of said ground terminal plate and said low impedance ground wire is coupled to said enclosure to provide a ground for said data surge protection module.

2. A data surge protector module, as defined in Claim 1, wherein said low impedance ground wire is a braided wire.

3. A data surge protector module, as defined in Claim 1, further comprising:

a) a first multi-conductor connector coupled to a like number of data input conductors; and

b) a first multi-pin connector, one first pin for each of said data input conductors, each of said first pins coupled to said printed circuit board whereby the joining of said first multi-conductor connector and said first multi-pin connector connects said data input conductors to said printed circuit board.

4. A data surge protector module, as defined in Claim 3, further comprising:

a) a second multi-conductor connector coupled to a like number of data output conductors; and

b) a second multi-pin connector, one second pin for each of said data output conductors, each of said second pins coupled to said printed circuit board whereby the joining of said second multi-conductor connector and said second multi-pin connector connects said data output conductors to said printed circuit board and said data input conductors.

5. A data surge protector module, as defined in Claim 3, wherein said first multi-pin connector is a quick connect, quick disconnect connector.

6. A data surge protection module, as defined in Claim 4, wherein said second multi-pin connector is a quick connect, quick disconnect connector.

7. A data surge protection module, as defined in Claim 4, wherein both of said first multi-pin and second multi-pin connectors are quick connect, quick disconnect connectors.

8. A data surge protection module, as defined in Claim 3, wherein said number of data input conductors is three.

9. A data surge protection module, as defined in claim 3, wherein said number of data input conductors is six.

10. A data surge protector module, as defined in Claim 8, wherein said number of data output conductors is three.

11. A data surge protector module, as defined in claim 9, wherein said number of data output conductors is six.

12. a data surge protection module comprising:

a) a housing;

b) a printed circuit board within said housing;

c) a first steering bridge on said printed circuit board coupled to a first data input conductor and a first data output conductor and a second data input conductor and a second data output conductor to direct electrical pulses between said first data input conductor and said second data output conductor;

d) a second steering bridge on said printed circuit board coupled to said first steering bridge and a third data output conductor; and

e) a diode coupled across said first and second steering bridges.

13. A data surge protection module, as defined in Claim 12, wherein said second data input conductor is coupled to said first data input conductor and to a grounded shield and said third data output conductor is connected to said grounded shield whereby said data surge protection module is surge protected between said first and second data output conductors and between each of said first and second data output conductors and said third data output conductor.

14. A data surge protection module, as defined in Claim 13, further comprising:

a) a first fuse in said first data input conductor; and

b) a second fuse in said second data input conductor.

15. A data surge protection module, as defined in Claim 14, wherein said first and second fuses are resettable.

16. A data surge protection module, as defined in Claim 14, wherein said first and second fuses are resettable positive temperature coefficient fuses.